

The diagram illustrates the interconnections of a computer system's memory and processing components. At the top, **Programmable Memory 14** and **Read Only Memory 16** are connected to a common bus. This bus leads to the **Memory Controller 12**. The Memory Controller is also connected to the **Processor 10**. A **Power On Reset** signal is connected to the **Reset** input of the Processor and the **R** (Reset) input of a latch (18). The latch (18) is an SR latch with inputs **S** (Set) and **R** (Reset), and outputs **Q** and **Q̄** (Q-bar). The **Q** output of the latch is connected to the **Access Latch Reset** input of the Processor. The **Q̄** output of the latch is connected to the **Reset** input of the Memory Controller.

## Figure 1

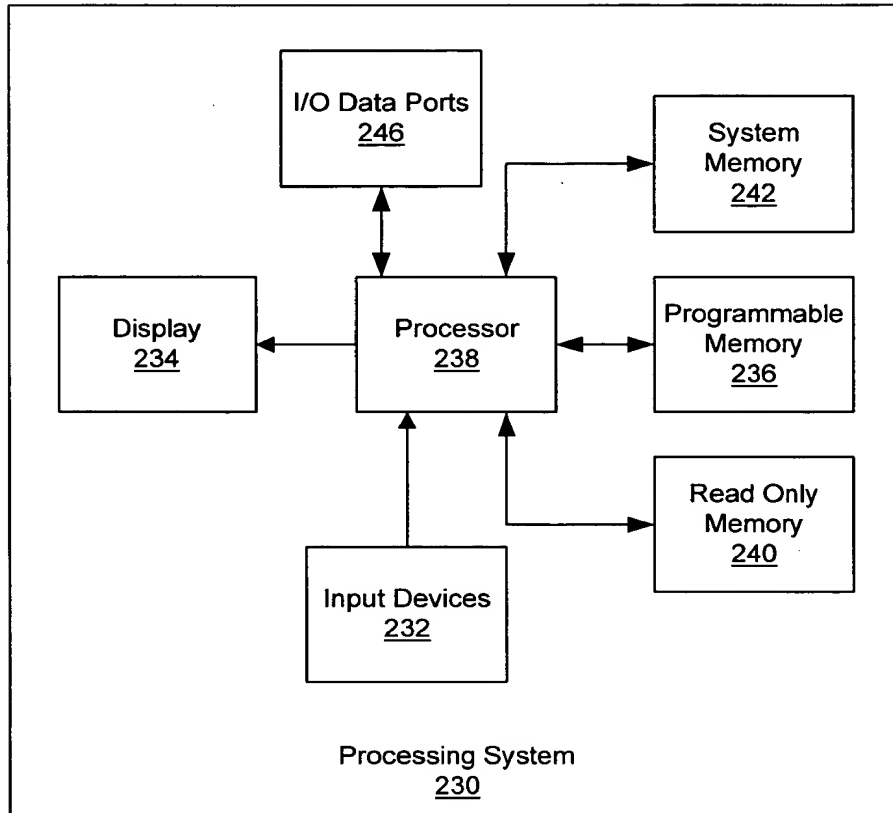


Figure 2

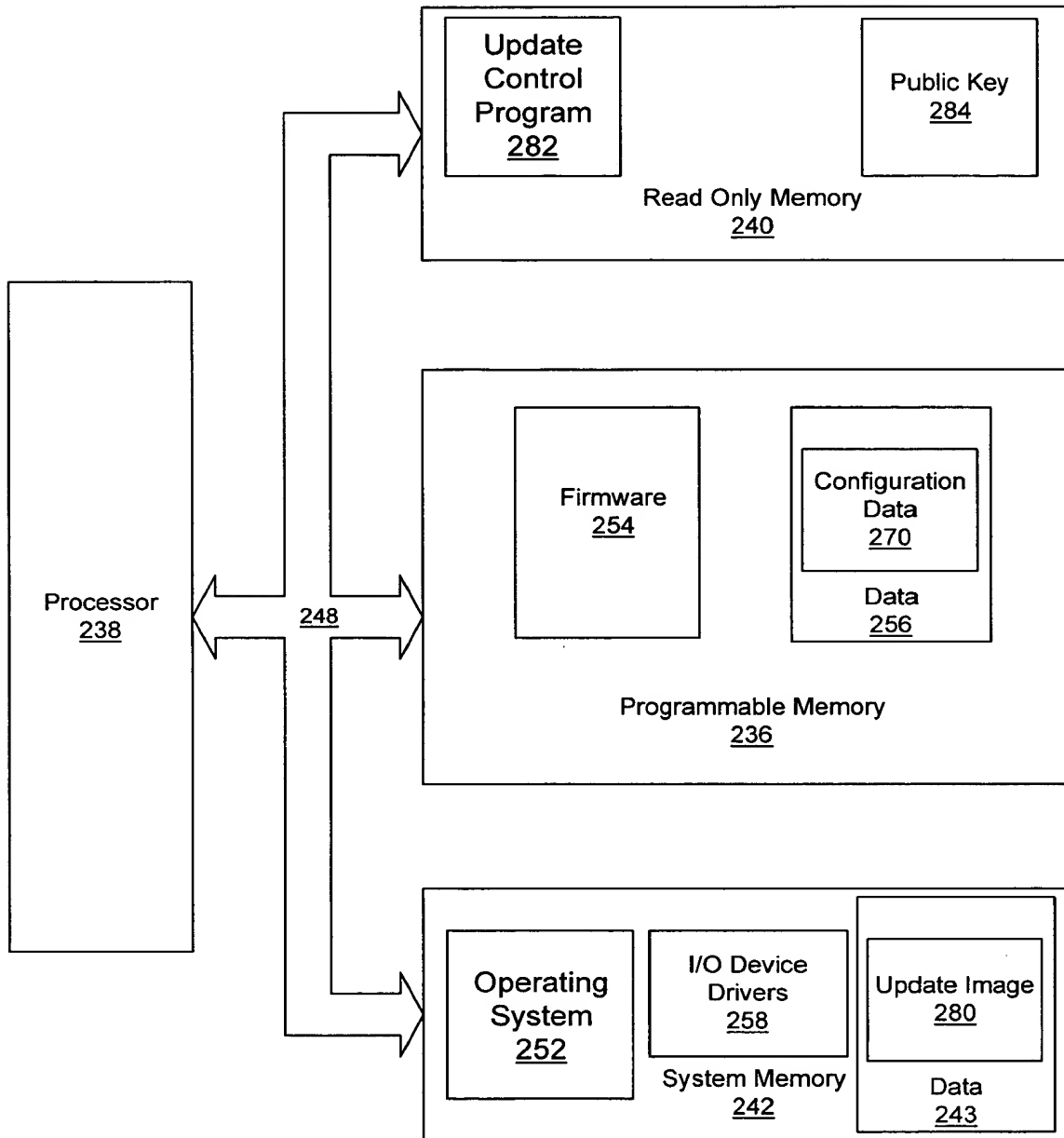


Figure 3

### Figure 4A

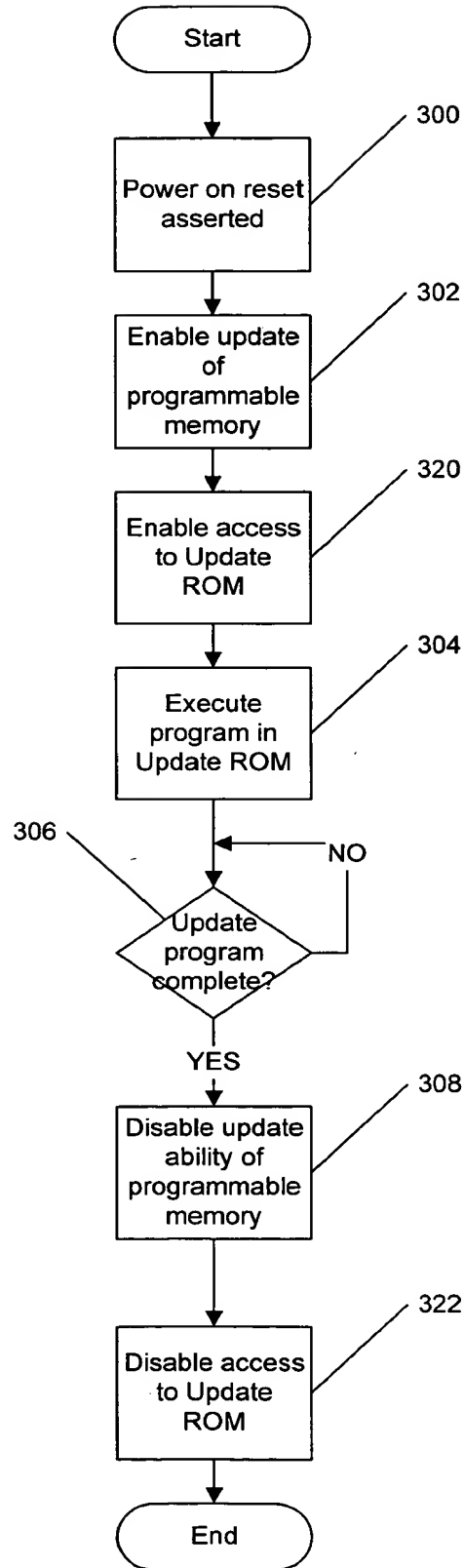
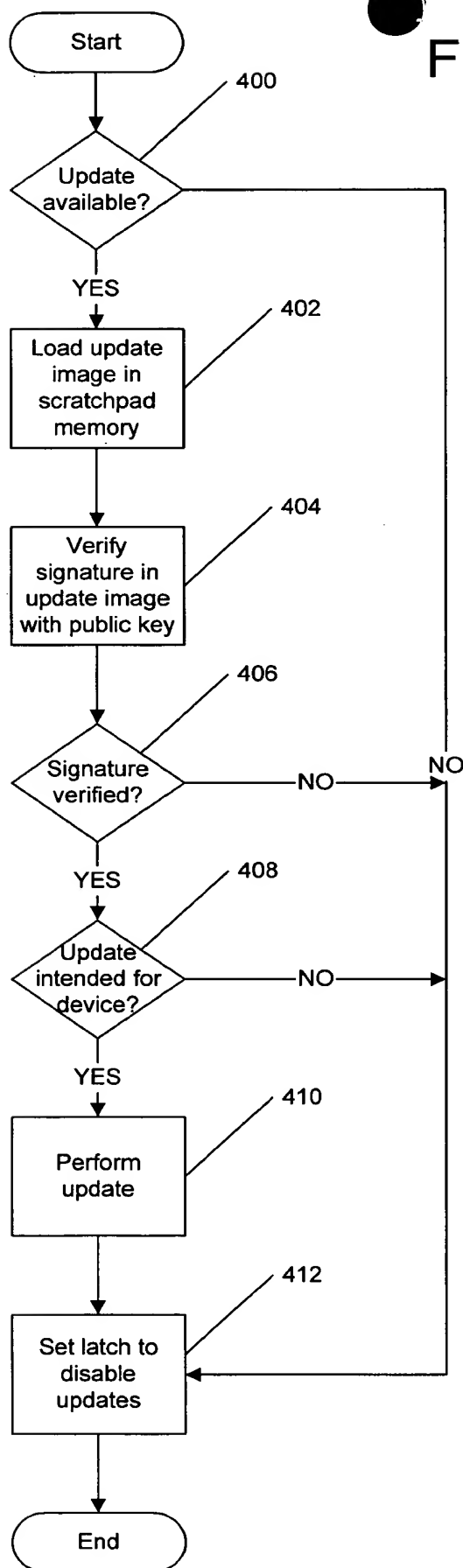
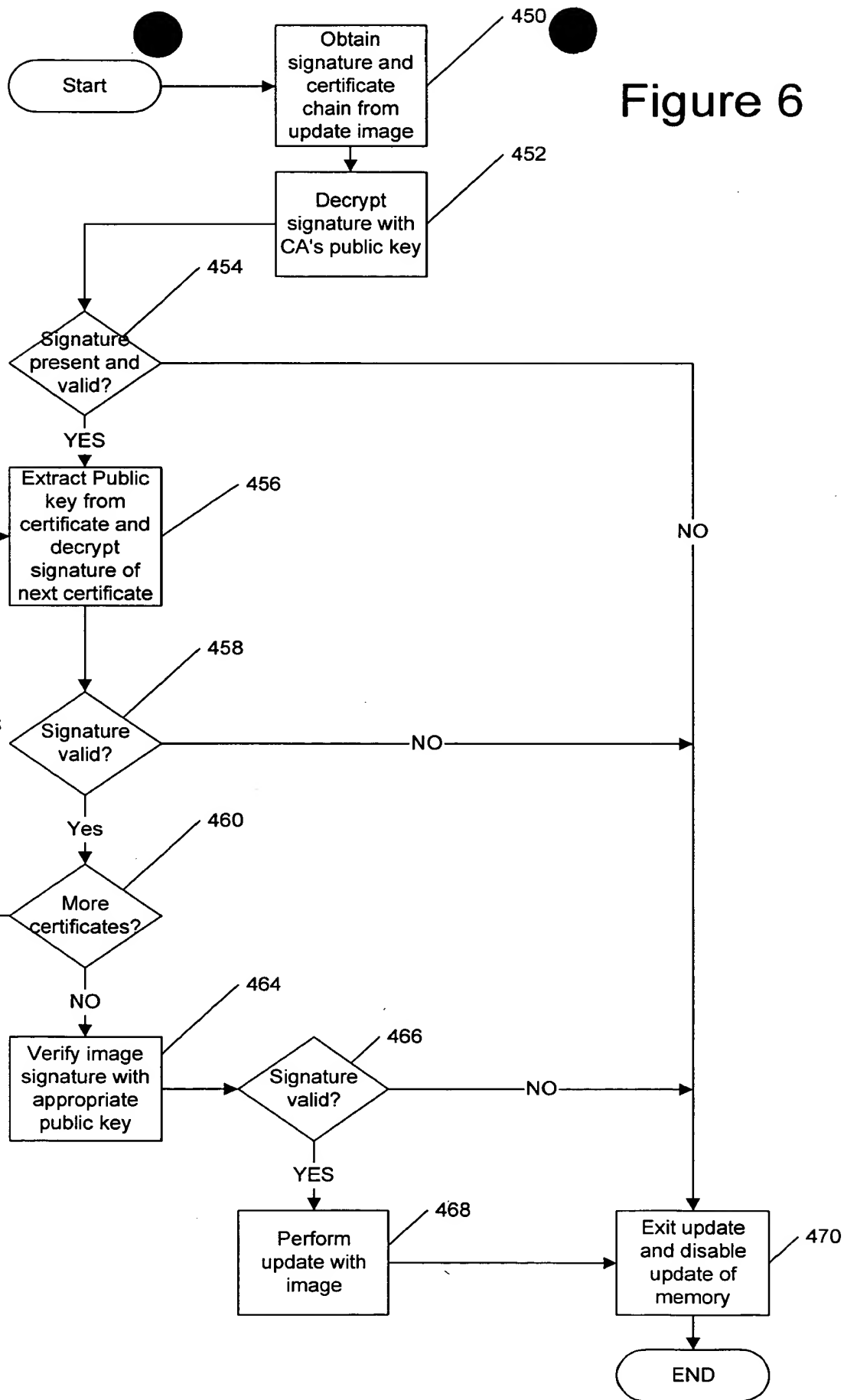


Figure 4B

### Figure 5





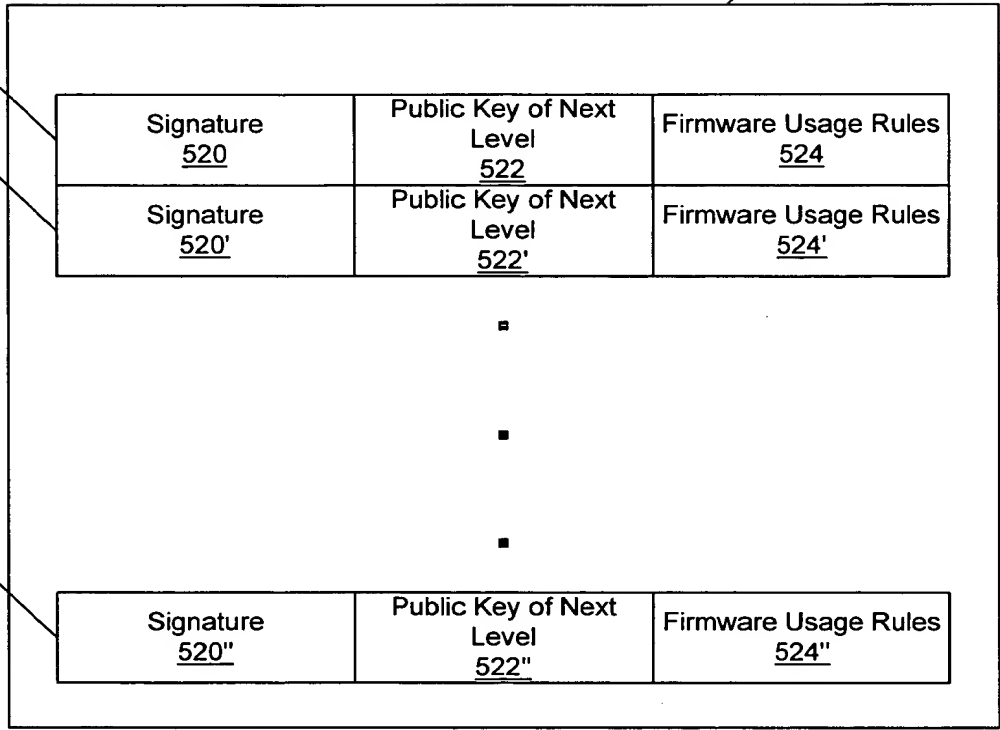


Figure 7



```
graph TD; Start([Start]) --> 600[Develop firmware update for multiple systems/functions]; 600 --> 602[Sign firmware update image]; 602 --> 604[Provide update authority's certificate with any firmware update conditions in a certificate extension field(s)]; 604 --> 606[Provide brand certificate with any firmware update extensions and with the brand's public key]; 606 --> 608[Provide manufacturer's certificate with any firmware update extensions and with the manufacturer's public key]; 608 --> 610[Provide root certificate authority's certificate with the certificate authority's public key]; 610 --> 612[Distribute firmware update]; 612 --> End([End]);
```

```

graph TD
    Start([Start]) --> Input[Input]
    Input --> Process[Process]
    Process --> Output[Output]
    Output --> End([End])

```

## Figure 9

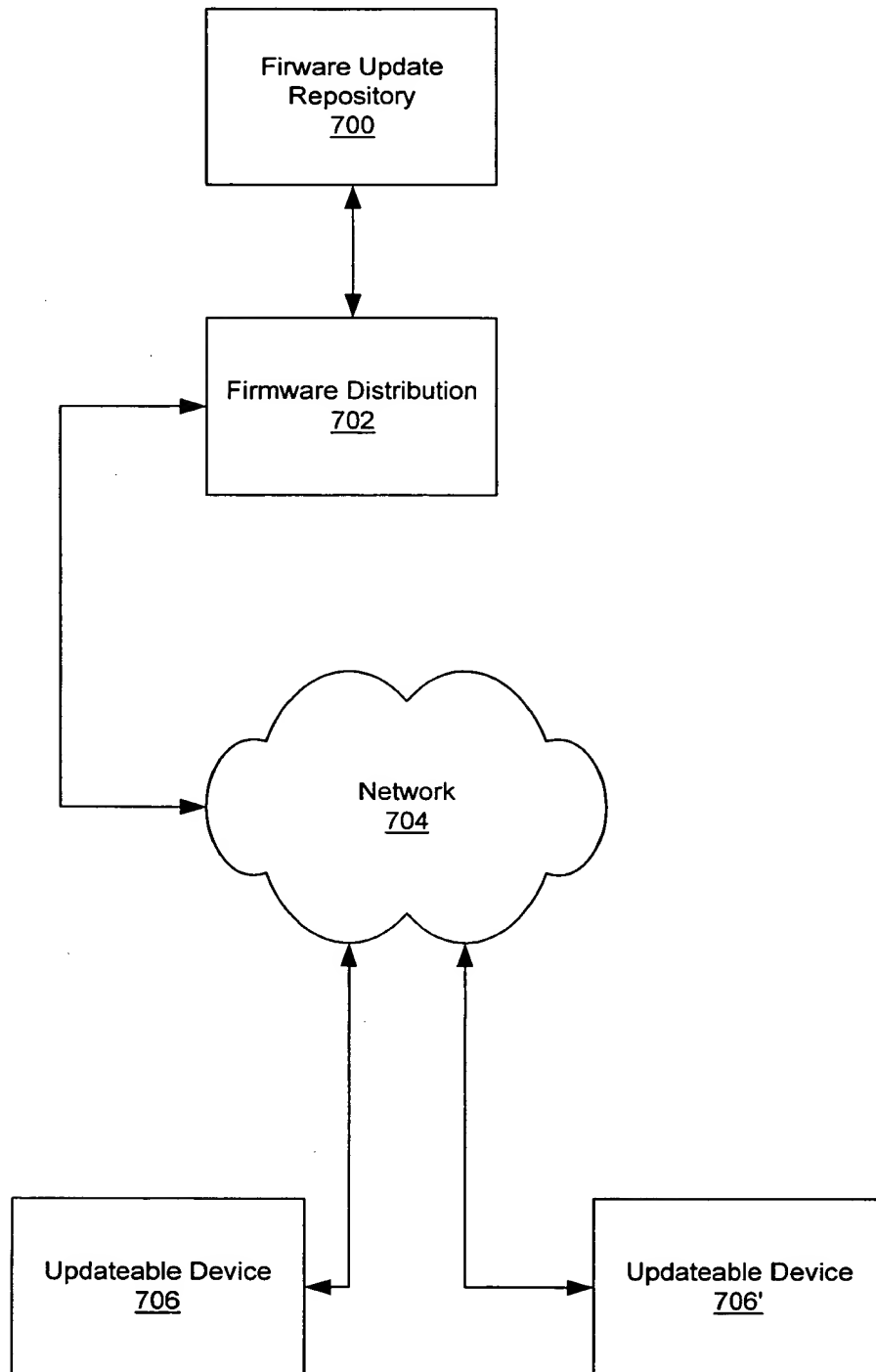


Figure 10

```
graph TD; Start([Start]) --> 710[Distribute generic devices with functions defined by the firmware in the device and having a secure firmware update capability]; 710 --> 712[Distribute firmware updates to define the functions of the devices based on a device level authorization]; 712 --> 714[Apply device level firmware updates to the generic devices to provide differing levels of functionality for the devices]; 714 --> End([End]);
```

Flowchart 700 illustrates a method for distributing generic devices with functions defined by the firmware in the device and having a secure firmware update capability. The process begins with a **Start** terminal, leading to step **710**: **Distribute generic devices with functions defined by the firmware in the device and having a secure firmware update capability**. This step leads to step **712**: **Distribute firmware updates to define the functions of the devices based on a device level authorization**. Step 712 leads to step **714**: **Apply device level firmware updates to the generic devices to provide differing levels of functionality for the devices**. Finally, step 714 leads to an **End** terminal.

```

graph TD
    End([End])
  
```